

10/052549

~~C. J. E. S.~~
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,809,385 B2

Issue Date: October 26, 2004

Inventor(s): Akihiko EBINA and Yutaka MARUO

Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING
NONVOLATILE SEMICONDUCTOR MEMORY DEVICES HAVING
CONTROL GATES CONNECTED TO COMMON CONTACT SECTION

Docket No.: 111779

REQUEST FOR CERTIFICATE OF CORRECTION UNDER RULE 323

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**Certificate
JAN 27 2005
of Correction**

Sir:

It is respectfully requested that a Certificate of Correction be issued in order to correct the error(s) specified in the attached copy of Certificate of Correction Form PTO-1050.

Our Check No. 162483 in the amount of One Hundred Dollars (\$100.00) is attached in accordance with the provisions of 37 C.F.R. §1.323 and §1.20(a). In the event of any underpayment or overpayment, please debit or credit Deposit Account No. 15-0461 as needed in order to ensure prompt issuance of a Certificate of Correction. Two duplicate copies of this Request are attached.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

Randi B. Isaacs
Registration No. 56,046

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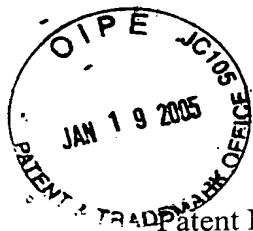
JAO:RBI/cfr

8 JAN 2005

Date: January 19, 2005

OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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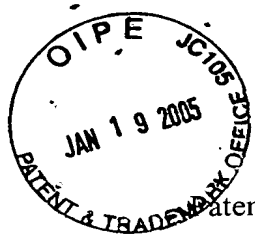
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PATENT NO : 6,809,385 B2
DATED : October 26, 2004
INVENTOR(S) : Akihiko Ebina; Yutaka Maruo

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page

Replace (73) Assignee as follows:

--(73) Assignee: Seiko Epson Corporation, Tokyo (JP); Halo LSI Design & Device Technology, Inc., Wappingers Falls, NY (US)--

MAILING ADDRESS OF SENDER: OLIFF & BERRIDGE, PLC (111779 RBI/SCG)
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